



Atty. Dkt. No. 040373-0287

Applicant: Yasuyuki MORISHITA  
Title: SEMICONDUCTOR DEVICE  
Appl. No.: 09/621,614  
Filing Date: 07/21/00  
Examiner: Nadav, Ori  
Art Unit: 2811

**AMENDMENT AND REQUEST FOR RECONSIDERATION**  
**UNDER 37 C.F.R. § 1.111**

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Commissioner for Patents  
Box Non-Fee Amendment  
Washington, D.C. 20231

Sir:

In reply to the Office Action dated September 26, 2001, please amend the above-identified application as follows:

**In the Specification:**

On page 1, paragraph beginning at line 11, delete and insert the following:

In a semiconductor integrated circuit device, an input protection circuit section or an output protection circuit section (referred to as an input/output protection circuit section, hereinafter) is set between an input terminal or an output terminal (referred to as an input/output terminal, hereinafter) and an internal circuit in order to protect circuit elements inside from failure which may be brought about by an ESD or the like applied to the input/output terminal.

On page 17, paragraph beginning at line 21, delete and insert the following:

Fig. 8 is a schematic cross-sectional view showing a protection circuit section of a Comparative Example.